

SPEC® CFP2006 Result

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Fujitsu

PRIMEQUEST 2800E2, Intel Xeon E7-8890 v3, 2.50 GHz

SPECfp®_rate2006 = 1030

SPECfp_rate_base2006 = 999

CPU2006 license: 19

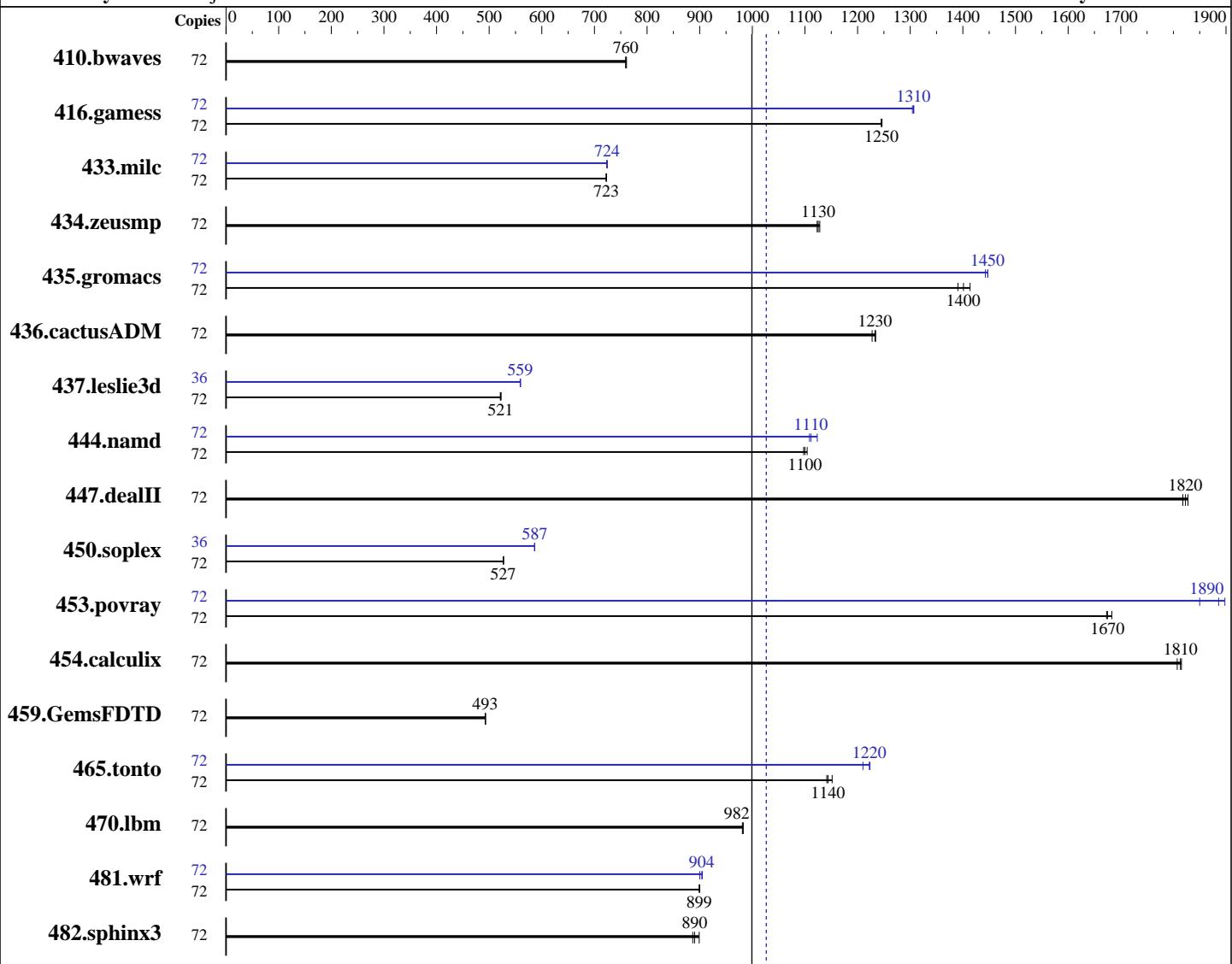
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2015

Hardware Availability: May-2015

Software Availability: Mar-2015



SPECfp_rate_base2006 = 999

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Hardware

CPU Name: Intel Xeon E7-8890 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4,6,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
 Compiler: 3.10.0-229.el7.x86_64
 C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 2 x SATA, 600 GB, 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	72	<u>1288</u>	<u>760</u>	1286	761	1288	759	72	<u>1288</u>	<u>760</u>	1286	761	1288	759
416.gamess	72	1132	1240	1131	1250	<u>1132</u>	<u>1250</u>	72	<u>1079</u>	<u>1310</u>	1081	1300	<u>1079</u>	<u>1310</u>
433.milc	72	914	723	<u>915</u>	<u>723</u>	915	722	72	914	723	912	724	<u>913</u>	<u>724</u>
434.zeusmp	72	<u>582</u>	<u>1130</u>	581	1130	583	1120	72	<u>582</u>	<u>1130</u>	581	1130	583	1120
435.gromacs	72	364	1410	<u>367</u>	<u>1400</u>	370	1390	72	<u>355</u>	<u>1450</u>	355	1450	356	1440
436.cactusADM	72	701	1230	<u>698</u>	<u>1230</u>	697	1230	72	701	1230	<u>698</u>	<u>1230</u>	697	1230
437.leslie3d	72	1298	521	1295	523	<u>1298</u>	<u>521</u>	36	<u>605</u>	<u>559</u>	605	560	606	559
444.namd	72	<u>525</u>	<u>1100</u>	523	1100	526	1100	72	514	1120	<u>519</u>	<u>1110</u>	521	1110
447.dealII	72	451	1830	453	1820	<u>452</u>	<u>1820</u>	72	451	1830	453	1820	<u>452</u>	<u>1820</u>
450.soplex	72	<u>1140</u>	<u>527</u>	1138	528	1140	527	36	512	586	<u>512</u>	<u>587</u>	512	587
453.povray	72	229	1670	228	1680	<u>229</u>	<u>1670</u>	72	202	1900	<u>203</u>	<u>1890</u>	207	1850
454.calculix	72	329	1810	<u>328</u>	<u>1810</u>	327	1820	72	329	1810	<u>328</u>	<u>1810</u>	327	1820
459.GemsFDTD	72	<u>1551</u>	<u>493</u>	1552	492	1549	493	72	<u>1551</u>	<u>493</u>	1552	492	1549	493
465.tonto	72	<u>619</u>	<u>1140</u>	615	1150	621	1140	72	<u>579</u>	1220	585	1210	<u>580</u>	<u>1220</u>
470.lbm	72	1007	982	1008	981	<u>1007</u>	<u>982</u>	72	1007	982	1008	981	<u>1007</u>	<u>982</u>
481.wrf	72	<u>894</u>	<u>899</u>	894	899	894	900	72	<u>890</u>	<u>904</u>	889	905	893	900
482.sphinx3	72	1561	899	<u>1576</u>	<u>890</u>	1582	887	72	1561	899	<u>1576</u>	<u>890</u>	1582	887

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Energy Performance = Performance

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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

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Tested with SPEC CPU2006 v1.2.

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