

White Paper FUJITSU Server PRIMERGY Performance Report PRIMERGY TX1320 M3

This document contains a summary of the benchmarks executed for the FUJITSU Server PRIMERGY TX1320 M3.

The PRIMERGY TX1320 M3 performance data are compared with the data of other PRIMERGY models and discussed. In addition to the benchmark results, an explanation has been included for each benchmark and for the benchmark environment.



Contents

Document history	2
Technical data	
SPECcpu2006	
SPECpower_ssj2008	
STREAM	
Literature	
Contact	_

Document history

Version 1.0 (2017-06-02)

New:

- Technical data
- SPECcpu2006
 Measurements with Celeron G3930, Pentium G4560, Core i3-7100 and Intel[®] Xeon[®] Processor E3-1200 v6 Product Family
- SPECpower_ssj2008 Measurement with Xeon E3-1230 v6
- STREAM
 Measurements with Celeron G3930, Pentium G4560, Core i3-7100 and Intel[®] Xeon[®] Processor E3-1200 v6 Product Family

Technical data

PRIMERGY TX1320 M3





Decimal prefixes according to the SI standard are used for measurement units in this white paper (e.g. 1 GB = 10^9 bytes). In contrast, these prefixes should be interpreted as binary prefixes (e.g. 1 GB = 2^{30} bytes) for the capacities of caches and memory modules. Separate reference will be made to any further exceptions where applicable.

Model	PRIMERGY TX1320 M3
Model versions	PY TX1320M3/LFF PY TX1320M3/SFF
Form factor	Tower server
Chipset	Intel® C236
Number of sockets	1
Processor type	Intel [®] Celeron [®] G3930 Intel [®] Pentium [®] G4560 Intel [®] Core [™] i3-7100 Intel [®] Xeon [®] Processor E3-1200 v6 Product Family
Number of memory slots	4
Maximum memory configuration	64 GB
Onboard LAN controller	2 x 1 Gbit/s
Onboard HDD controller	Controller with RAID 0, RAID 1 or RAID 10 for up to 4 SATA HDDs
PCI slots	2 x PCI-Express 3.0 x8 1 x PCI-Express 3.0 x4 1 x PCI-Express 3.0 x1 (mech. x4)
Max. number of internal hard disks	PY TX1320M3/LFF: 2 PY TX1320M3/SFF: 6

Processors (since system release)							
Processor	Cores	Threads	Cache [MB]	Rated Frequency [Ghz]	Max. Turbo Frequency [Ghz]	Max. Memory Frequency [MHz]	TDP
Celeron G3930	2	2	2	2.90	n/a	2133	51
Pentium G4560	2	4	3	3.50	n/a	2400	54
Core i3-7100	2	4	3	3.90	n/a	2400	51
Xeon E3-1220 v6	4	4	8	3.00	3.50	2400	72
Xeon E3-1225 v6	4	4	8	3.30	3.70	2400	73
Xeon E3-1230 v6	4	8	8	3.50	3.90	2400	72
Xeon E3-1240 v6	4	8	8	3.70	4.10	2400	72
Xeon E3-1270 v6	4	8	8	3.80	4.20	2400	72
Xeon E3-1280 v6	4	8	8	3.90	4.20	2400	72

All the processors of Intel[®] Xeon[®] Processor E3-1200 v6 Product Family that can be ordered with the PRIMERGY TX1320 M3 support Intel[®] Turbo Boost Technology 2.0. This technology allows you to operate the processor with higher frequencies than the nominal frequency. Listed in the processor table is "Max. Turbo Frequency" for the theoretical frequency maximum with only one active core per processor. The maximum frequency that can actually be achieved depends on the number of active cores, the current consumption, electrical power consumption and the temperature of the processor.

As a matter of principle Intel does not guarantee that the maximum turbo frequency will be reached. This is related to manufacturing tolerances, which result in a variance regarding the performance of various examples of a processor model. The range of the variance covers the entire scope between the nominal frequency and the maximum turbo frequency.

The turbo functionality can be set via BIOS option. Fujitsu generally recommends leaving the "Turbo Mode" option set at the standard setting "Enabled", as performance is substantially increased by the higher frequencies. However, since the higher frequencies depend on general conditions and are not always guaranteed, it can be advantageous to disable the "Turbo Mode" option for application scenarios with intensive use of AVX instructions and a high number of instructions per clock unit, as well as for those that require constant performance or lower electrical power consumption.

Memory modules (since system release)								
Memory module	Capacity [GB]	Ranks	Bit width of the memory chips	Frequency [MHz]	Low voltage	Load reduced	Registered	ECC
4GB (1x4GB) 1Rx8 DDR4-2400 U ECC	4	1	8	2400				✓
8GB (1x8GB) 1Rx8 DDR4-2400 U ECC	8	1	8	2400				✓
16GB (1x16GB) 2Rx8 DDR4-2400 U ECC	16	2	8	2400				✓

Power supplies (since system release)	Max. number
Standard PSU 250W	1

Some components may not be available in all countries or sales regions.

Detailed technical information is available in the data sheet PRIMERGY TX1320 M3.

SPECcpu2006

Benchmark description

SPECcpu2006 is a benchmark which measures the system efficiency with integer and floating-point operations. It consists of an integer test suite (SPECint2006) containing 12 applications and a floating-point test suite (SPECfp2006) containing 17 applications. Both test suites are extremely computing-intensive and concentrate on the CPU and the memory. Other components, such as Disk I/O and network, are not measured by this benchmark.

SPECcpu2006 is not tied to a special operating system. The benchmark is available as source code and is compiled before the actual measurement. The used compiler version and their optimization settings also affect the measurement result.

SPECcpu2006 contains two different performance measurement methods: the first method (SPECint2006 or SPECfp2006) determines the time which is required to process single task. The second method (SPECint_rate2006 or SPECfp_rate2006) determines the throughput, i.e. the number of tasks that can be handled in parallel. Both methods are also divided into two measurement runs, "base" and "peak" which differ in the use of compiler optimization. When publishing the results the base values are always used; the peak values are optional.

Benchmark	Arithmetics	Туре	Compiler optimization	Measurement result	Application
SPECint2006	integer	peak	aggressive	Spood	aingle threeded
SPECint_base2006	integer	base	conservative	Speed	single-threaded
SPECint_rate2006	integer	peak	aggressive	Throughput	multi-threaded
SPECint_rate_base2006	integer	base	conservative	- Throughput	muiti-timeaded
SPECfp2006	floating point	peak	aggressive	Chood	aingle threeded
SPECfp_base2006	floating point	base	conservative	Speed	single-threaded
SPECfp_rate2006	floating point	peak	aggressive	Throughput	multi-threaded
SPECfp_rate_base2006	floating point	base	conservative	Throughput	multi-timeaded

The measurement results are the geometric average from normalized ratio values which have been determined for individual benchmarks. The geometric average - in contrast to the arithmetic average - means that there is a weighting in favour of the lower individual results. Normalized means that the measurement is how fast is the test system compared to a reference system. Value "1" was defined for the SPECint_base2006-, SPECint_rate_base2006, SPECfp_base2006 and SPECfp_rate_base2006 results of the reference system. For example, a SPECint_base2006 value of 2 means that the measuring system has handled this benchmark twice as fast as the reference system. A SPECfp_rate_base2006 value of 4 means that the measuring system has handled this benchmark some 4/[# base copies] times faster than the reference system. "# base copies" specify how many parallel instances of the benchmark have been executed.

Not every SPECcpu2006 measurement is submitted by us for publication at SPEC. This is why the SPEC web pages do not have every result. As we archive the log files for all measurements, we can prove the correct implementation of the measurements at any time.

Page 6 (15)

Benchmark environment

System Under Test	(SUT)
Hardware	
Model	PRIMERGY TX1330 M3
Processor	Celeron G3930 Pentium G4560 Core i3-7100 Intel® Xeon® Processor E3-1200 v6 Product Family
Memory	4 x 16GB (1x16GB) 2Rx8 DDR4-2400 U ECC
Software	
BIOS settings	SPECint2006/SPECint_base2006/SPECfp2006/SPECfp_base2006: Hyper-threading = Disabled
Operating system	SUSE Linux Enterprise Server 12 SP2 (x86_64)
Operating system settings	cpupower -c all frequency-set -g performance cpupower idle-set -d 2 cpupower idle-set -d 3 cpupower idle-set -d 4 echo always > /sys/kernel/mm/transparent_hugepage/enabled SPECint2006/SPECint_base2006/SPECfp2006/SPECfp_base2006 :
Compiler	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux

Some components may not be available in all countries or sales regions.

http://ts.fujitsu.com/primergy

Benchmark results

In terms of processors the benchmark result depends primarily on the size of the processor cache, the support for Hyper-Threading, the number of processor cores and on the processor frequency. In the case of processors with Turbo mode the number of cores, which are loaded by the benchmark, determines the maximum processor frequency that can be achieved. In the case of single-threaded benchmarks, which largely load one core only, the maximum processor frequency that can be achieved is higher than with multi-threaded benchmarks.

The results marked (est.) are estimates.

Processor	SPECint_base2006	SPECint2006	SPECint_rate_base2006	SPECint_rate2006
Celeron G3930			83.5	86.5
Pentium G4560			123	128
Core i3-7100			140	147
Xeon E3-1220 v6			199(est.)	208(est.)
Xeon E3-1225 v6			208(est.)	218(est.)
Xeon E3-1230 v6			255(est.)	267(est.)
Xeon E3-1240 v6			264(est.)	277(est.)
V 50 1070 0			269(est.)	281(est.)
Xeon E3-1270 v6			209(651.)	201(631.)

Processor	SPECfp_base2006	SPECfp2006	SPECfp_rate_base2006	SPECfp_rate2006
Celeron G3930	63.2	63.7	92.8	93.8
Pentium G4560	76.0	76.7	122	123
Core i3-7100	92.4	93.7	142	145
Xeon E3-1220 v6	93.3(est.)	93.6(est.)	184(est.)	187(est.)
Xeon E3-1225 v6	96.8(est.)	97.0(est.)	189(est.)	192(est.)
Xeon E3-1230 v6	100(est.)	101(est.)	202(est.)	204(est.)
Xeon E3-1240 v6	103(est.)	103(est.)	205(est.)	209(est.)
Xeon E3-1270 v6	106(est.)	106(est.)	207(est.)	211(est.)
Xeon E3-1280 v6	105	106	207	211

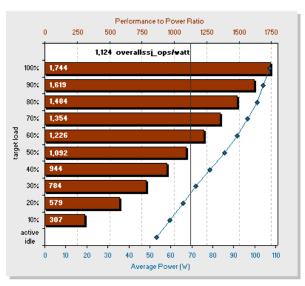
SPECpower_ssj2008

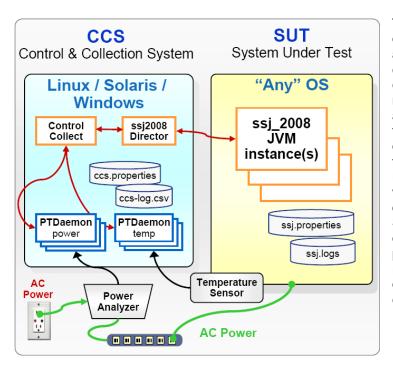
Benchmark description

SPECpower_ssj2008 is the first industry-standard SPEC benchmark that evaluates the power and performance characteristics of a server. With SPECpower_ssj2008 SPEC has defined standards for server power measurements in the same way they have done for performance.

The benchmark workload represents typical server-side Java business applications. The workload is scalable, multi-threaded, portable across a wide range of platforms and easy to run. The benchmark tests CPUs, caches, the memory hierarchy and scalability of symmetric multiprocessor systems (SMPs), as well as the implementation of Java Virtual Machine (JVM), Just In Time (JIT) compilers, garbage collection, threads and some aspects of the operating system.

SPECpower_ssj2008 reports power consumption for servers at different performance levels — from 100% to "active idle" in 10% segments — over a set period of time. The graduated workload recognizes the fact that processing loads and power consumption on servers vary substantially over the course of days or weeks. To compute a power-performance metric across all levels, measured transaction throughputs for each segment are added together and then divided by the sum of the average power consumed for each segment. The result is a figure of merit called "overall ssj_ops/watt". This ratio provides information about the energy efficiency of the measured server. The defined measurement standard enables customers to compare it with other configurations and servers measured SPECpower ssi2008. The diagram shows a typical graph of a SPECpower_ssj2008 result.





The benchmark runs on a wide variety of operating systems and hardware architectures and does not require extensive client or storage infrastructure. The minimum equipment for SPEC-compliant testing is two networked computers, plus a power analyzer and a temperature sensor. One computer is the System Under Test (SUT) which runs one of the supported operating systems and the JVM. The JVM provides the environment required to run the SPECpower ssi2008 workload which is implemented in Java. The other computer is a "Control & Collection System" (CCS) which controls the operation of the benchmark and captures the power, performance and temperature readings for reporting. The diagram provides an overview of the basic structure of the benchmark configuration and the various components.

Benchmark environment

System Under Test (S	SUT)
Hardware	
Model	PRIMERGY TX1320 M3
Model version	PY TX1320M3/LFF
Processor	Xeon E3-1230 v6
Memory	2 x 8GB (1x8GB) 1Rx8 DDR4-2400 U ECC
Network-Interface	Onboard LAN-Controller (2 port used)
Disk-Subsystem	Onboard SATA controller 1 x SSD SATA 6G 64GB DOM N H-P
Power Supply Unit	1 x Standard PSU 250W
Software	
BIOS	R1.0.0
BIOS settings	Hardware Prefetcher = Disabled Adjacent Cache Line Prefetch = Disabled DCU Streamer Prefetcher = Disabled ASPM Support = Auto Turbo Mode = Disabled LAN Controller = LAN 1 Intel Virtualization Technology = Disabled SATA Port 1 = Disabled SATA Port 2 = Disabled SATA Port 3 = Disabled SATA Port 4 = Disabled SATA Port 5 = Disabled SATA Port 5 = Disabled Management LAN = Disabled
Firmware	8.64F
Operating system Operating system settings	Microsoft Windows Server 2012 R2 Standard Using the local security settings console, "lock pages in memory" was enabled for the user running the benchmark. Power Management: Enabled ("Fujitsu Enhanced Power Settings" power plan) Set "Turn off hard disk after = 1 Minute" in OS. Benchmark was started via Windows Remote Desktop Connection. Each JVM instance was affinitized to two logical processors.
JVM	Oracle Java HotSpot(TM) 64-Bit Server VM (build 24.80-b11, mixed mode), version 1.7.0_80
JVM settings	-server -Xmn9500m -Xms11000m -Xmx11000m -XX:SurvivorRatio=1 - XX:TargetSurvivorRatio=99 -XX:AllocatePrefetchDistance=256 -XX:AllocatePrefetchLines=4 - XX:LoopUnrollLimit=45 -XX:InitialTenuringThreshold=12 -XX:MaxTenuringThreshold=15 - XX:ParallelGCThreads=2 -XX:InlineSmallCode=3900 -XX:MaxInlineSize=270 - XX:FreqInlineSize=2500 -XX:+AggressiveOpts -XX:+UseLargePages -XX:+UseParallelOldGC

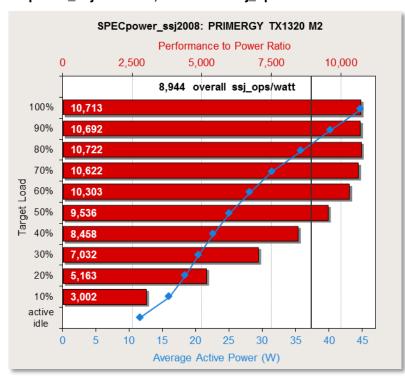
Some components may not be available in all countries or sales regions.

http://ts.fujitsu.com/primergy Page 10 (15)

Benchmark results

The PRIMERGY TX1320 M3 achieved the following result:

SPECpower_ssj2008 = 10,427 overall ssj_ops/watt



The adjoining diagram shows the result of the configuration described above. The red horizontal bars show the performance to power ratio in ssi ops/watt (upper x-axis) for each target load level tagged on the y-axis of the diagram. The blue line shows the run of the curve for the average power consumption (bottom x-axis) at each target load level marked with a small rhomb. The black vertical line shows the benchmark result of 10,427 overall ssj_ops/watt for the PRIMERGY TX1320 M3. This is the quotient of the sum of the transaction throughputs for each load level and the sum of the average power consumed for each measurement interval.

The following table shows the benchmark results for the throughput in ssj_ops, the power consumption in watts and the resulting energy efficiency for each load level.

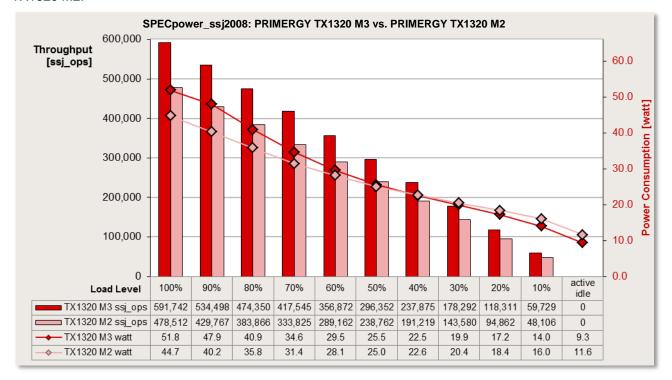
Performance		Power	Energy Efficiency	
Target Load	ssj_ops	Average Power (W)	ssj_ops/watt	
100 %	591,742	51.8	11,421	
90 %	534,498	47.9	11,154	
80 %	474,350	40.9	11,606	
70 %	417,545	34.6	12,070	
60 %	356,872	29.5	12,103	
50 %	296,352	25.5	11,629	
40 %	237,875	22.5	10,576	
30 %	178,292	19.9	8,958	
20 %	118,311	17.2	6,862	
10 %	59,729	14.0	4,254	
Active Idle	0	9.33	0	
	∑ssj_ops / ∑power = 10,427			

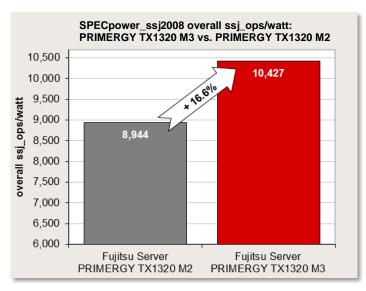


The PRIMERGY TX1320 M3 achieved a new class record with this result (date: April 12, 2017). Thus, the PRIMERGY TX1320 M3 proves itself to be the most energy-efficient 1-socket server in the world. For the latest SPECpower_ssj2008 benchmark results, visit: http://www.spec.org/power_ssj2008/results.

http://ts.fujitsu.com/primergy Page 11 (15)

The following diagram shows for each load level the power consumption (on the right y-axis) and the throughput (on the left y-axis) of the PRIMERGY TX1320 M3 compared to the predecessor PRIMERGY TX1320 M2.





The comparison with PRIMERGY TX1320 M2 which has been the most energy efficient in the category of 1 socet server the advantage of the PRIMERGY TX1320 M3 in the field of energy efficiency evident.

Compared with TX1320 M2 of old system, TX1320 M3 achieves 16.6% superior energy efficiency.

STREAM

Benchmark description

STREAM is a synthetic benchmark that has been used for many years to determine memory throughput and which was developed by John McCalpin during his professorship at the University of Delaware. Today STREAM is supported at the University of Virginia, where the source code can be downloaded in either Fortran or C. STREAM continues to play an important role in the HPC environment in particular. It is for example an integral part of the HPC Challenge benchmark suite.

The benchmark is designed in such a way that it can be used both on PCs and on server systems. The unit of measurement of the benchmark is GB/s, i.e. the number of gigabytes that can be read and written per second.

STREAM measures the memory throughput for sequential accesses. These can generally be performed more efficiently than accesses that are randomly distributed on the memory, because the processor caches are used for sequential access.

Before execution the source code is adapted to the environment to be measured. Therefore, the size of the data area must be at least 12 times larger than the total of all last-level processor caches so that these have as little influence as possible on the result. The OpenMP program library is used to enable selected parts of the program to be executed in parallel during the runtime of the benchmark, consequently achieving optimal load distribution to the available processor cores.

During implementation the defined data area, consisting of 8-byte elements, is successively copied to four types, and arithmetic calculations are also performed to some extent.

Туре	Execution	Bytes per step	Floating-point calculation per step
COPY	a(i) = b(i)	16	0
SCALE	$a(i) = q \times b(i)$	16	1
SUM	a(i) = b(i) + c(i)	24	1
TRIAD	$a(i) = b(i) + q \times c(i)$	24	2

The throughput is output in GB/s for each type of calculation. The differences between the various values are usually only minor on modern systems. In general, only the determined TRIAD value is used as a comparison.

The measured results primarily depend on the clock frequency of the memory modules; the processors influence the arithmetic calculations.

This chapter specifies throughputs on a basis of 10 (1 GB/s = 10⁹ Byte/s).

Benchmark environment

System Under Test (SUT)					
Hardware					
Model	PRIMERGY TX1320 M3				
Processor	Celeron G3930 Pentium G4560 Core i3-7100 Intel® Xeon® Processor E3-1200 v6 Product Family				
Memory	16GB (1x16GB) 2Rx8 DDR4-2400 U ECC x 4				
Software					
Operating system	SUSE Linux Enterprise Server 12 SP2 (x86_64)				
Operating system settings	Transparent Huge Pages inactivated				
Compiler	Version 17.0.0.098 of Intel C++ Compiler for Linux				
Benchmark	Stream.c Version 5.10				

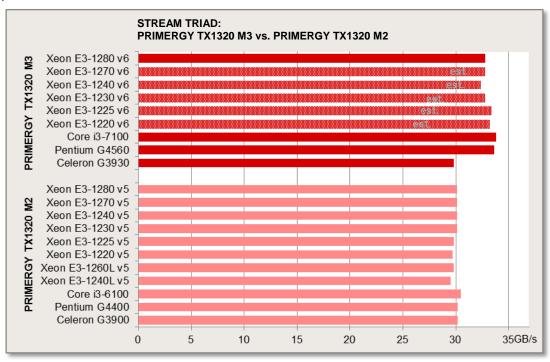
Some components may not be available in all countries or sales regions.

Benchmark results

The results marked (est.) are estimates.

Processor	Memory Frequency [MHz]	Max. Memory Bandwidth [GB/s]	Cores	Processor Frequency [GHz]	TRIAD [GB/s]
Celeron G3930	2133	34.1	2	2.90	29.8
Pentium G4560	2400	38.4	2	3.50	33.6
Core i3-7100	2400	38.4	2	3.90	33.8
Xeon E3-1220 v6	2400	38.4	4	3.00	33.2(est.)
Xeon E3-1225 v6	2400	38.4	4	3.30	33.4(est.)
Xeon E3-1230 v6	2400	38.4	4	3.50	32.8(est.)
Xeon E3-1240 v6	2400	38.4	4	3.70	32.4(est.)
Xeon E3-1270 v6	2400	38.4	4	3.80	32.8(est.)
Xeon E3-1280 v6	2400	38.4	4	3.90	32.8

The following diagram illustrates the throughput of the PRIMERGY TX1320 M3 in comparison to its predecessor, the PRIMERGY TX1320 M2.



http://ts.fujitsu.com/primergy Page 14 (15)

Literature

PRIMERGY Servers

http://primergy.com/

PRIMERGY TX1320 M3

This White Paper:

http://docs.ts.fujitsu.com/dl.aspx?id=a3de8400-2d5c-4641-a945-e245e165a080

http://docs.ts.fujitsu.com/dl.aspx?id=f8662db5-75c6-4b58-a757-41f6334bd752

Data sheet

http://docs.ts.fujitsu.com/dl.aspx?id=070d03d7-b7e3-406b-aeac-b442d047cbd7

PRIMERGY Performance

http://www.fujitsu.com/fts/x86-server-benchmarks

Performance of Server Components

http://www.fujitsu.com/fts/products/computing/servers/mission-critical/benchmarks/x86-components.html

RAID Controller Performance 2013

http://docs.ts.fujitsu.com/dl.aspx?id=e2489893-cab7-44f6-bff2-7aeea97c5aef

RAID Controller Performance 2016

http://docs.ts.fujitsu.com/dl.aspx?id=9845be50-7d4f-4ef7-ac61-bbde399c1014

Disk I/O: Performance of storage media and RAID controllers

Basics of Disk I/O Performance

http://docs.ts.fujitsu.com/dl.aspx?id=65781a00-556f-4a98-90a7-7022feacc602

Information about Iometer http://www.iometer.org

SPECcpu2006

http://www.spec.org/osg/cpu2006

Benchmark overview SPECcpu2006

http://docs.ts.fujitsu.com/dl.aspx?id=1a427c16-12bf-41b0-9ca3-4cc360ef14ce

SPECpower_ssj2008

http://www.spec.org/power_ssj2008

Benchmark Overview SPECpower_ssj2008

http://docs.ts.fujitsu.com/dl.aspx?id=166f8497-4bf0-4190-91a1-884b90850ee0

STREAM

http://www.cs.virginia.edu/stream/

Contact

FUJITSU

Website: http://www.fujitsu.com/

PRIMERGY Product Marketing

mailto:Primergy-PM@ts.fujitsu.com

PRIMERGY Performance and Benchmarks

mailto:primergy.benchmark@ts.fujitsu.com

© Copyright 2017 Fujitsu Technology Solutions. Fujitsu and the Fujitsu logo are trademarks or registered trademarks of Fujitsu Limited in Japan and other countries. Other company, product and service names may be trademarks or registered trademarks of their respective owners. Technical data subject to modification and delivery subject to availability. Any liability that the data and illustrations are complete, actual or correct is excluded. Designations may be trademarks and/or copyrights of the respective manufacturer, the use of which by third parties for their own purposes may infringe the rights of such owner. For further information see http://www.fujitsu.com/fts/resources/navigation/terms-of-use.html

2017-0x-xx WW EN

http://ts.fujitsu.com/primergy Page 15 (15)